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March 1, 2004

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## Application of

Applicants : Chandra V. Mouli and Ceredig Roberts

Serial No. : 09/648,044

Filed : August 25, 2000

Confirm No. : 6800

Title : **METHOD AND DEVICE TO REDUCE GATE-INDUCED DRAIN  
LEAKAGE (GIDL) CURRENT IN THIN GATE OXIDE MOSFETs**

Docket No. : MIO 0054 PA

Examiner : O. Nadav

Art Unit : 2811

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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William A. Jividen, Esq.

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Reg. No.

Sir:

**OFFICE COMMUNICATION**

This paper is being filed in response to the Office Communication of February 23, 2004 and having a non-fee response date of on or before March 23, 2004. Provided herewith, please find the corrected Amendment to the Claims section in accordance to 37 CFR 1.121, which provides a complete listing of all the claims presented in ascending numerical order.

Respectfully submitted,  
DINSMORE & SHOHL, L.L.P.

By

  
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